

**Amendments to the Specification**

Please replace the paragraph indicated below in its entirety with the replacement text as marked up.

***Specification, page 7, line 15 to page 8, line 2:***

Additional background information regarding logical partitioning can be found in the following commonly owned patents and patent applications, which are herein incorporated by reference: Serial No. 09/672,043, filed September 29, 2000, entitled *Technique for Configuring Processors in System With Logical Partitions*; Serial No. 09/346,206, filed July 1, 1999 U.S. Patent 6,436,671 to Doing et al., entitled *Apparatus for Supporting a Logically Partitioned Computer System (R0999-026) Generating Partition Corresponding Real Address in Partitioned Mode Supporting System*; Serial No. 09/314,769, filed May 19, 1999 U.S. Patent 6,467,007 to Armstrong et al., entitled *Processor Reset Generated Via Memory Access Interrupt*; Serial No. 09/314,541, filed May 19, 1999 U.S. Patent 6,681,240 to Armstrong et al., entitled *Apparatus and Method for Specifying Maximum Interactive Performance in a Logical Partition of a Computer Apparatus and Method for Specifying Maximum Interactive Performance in a Logical Partition of a Computer*; Serial No. 09/314,324, filed May 19, 1999, entitled *Management of a Concurrent Use License in a Logically Partitioned Computer*; Serial No. 09/314,214, filed May 19, 1999 U.S. Patent 6,691,146 to Armstrong et al., entitled *Logical Partition Manager and Method*; Serial No. 09/314,187, filed May 19, 1999 U.S. Patent 6,279,046 to Armstrong et al., entitled *Event-Driven Communications Interface for Logically Partitioned Computer*; U.S. Patent 5,659,786 to George et al.; and U.S. Patent 4,843,541 to Bean et al. The latter two patents describe implementations using the IBM S/360, S/370, S/390 and related architectures, while the remaining patents and applications describe implementations using the IBM AS/400 and related architectures.